

Prabhat Srivastava

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OBJECTIVES

Seeking for the dynamic & challenging position of professional job where I can help lead engineer to meet project requirement in a challenging environment.

EXPERIENCE

- ✓ Worked as an **Assistant Professor** in **B.N. College of Engineering & Technology, Lucknow** from Aug,2015 to Jan.,2022.
- ✓ Worked as a **Lecturer** in **Vishveshwaraya Group of Institutions, Gr. Noida** from **Mar,2011 to Aug,2012**.
- ✓ Worked as a **Lecturer** in **Institute of Technology & Management, Merrut** from Oct,2010 to Mar 2011.
- ✓ Worked as a **Lecturer** in **Babu Banarasi Das Institute of Technology & Management, Lucknow** from Dec,2009 to Oct,2010.
- ✓ Worked as **Timing Engineer** with **Teamleas Compony** in the project of Police Recruitment Board from Sep,2009 to Dec,2009.

ACADEMIC CREDENTIALS

1. Pursuing **Ph.D (Jan-2022)** in Electrical Engineering on title “**Coordinated Control and Communication in Smart Grid**” from Netaji Subhas Institute Of technology, Delhi aggregate of **8.25 GPA(Grade Point Average)**.
2. **M.Tech in Procss & Control** from Netaji Subhas Institute Of technology, Delhi (affiliated to DELHI UNIVERSITY) with aggregate of **8.23 GPA(Grade Point Average)**.
3. **B.Tech.** from **Electronics & Communication** from Ideal Institute of Technology, Ghaziabad (affiliated to UTTAR PRADESH TECHNICAL UNIVERSITY LUCKNOW) with **71.98%** aggregate.
4. **Intermediate** from Lucknow Public Inter College with **81%** marks in 2004 from U.P.BOARD.
5. **High School** from Lucknow Public Inter College with **67.17%** marks in 2002 from U.P.BOARD.

THESIS & RESEARCH PAPER

- ✓ Ph.D thesis is proposed to submit under the guidance of **Prof. Prerna Gaur** and **Dr. Alok Agrawal**.
- ✓ P. Srivastava, A. Agrawal and P. Gaur, "**An Investigation on Internet of Things: Smart Grid Technology Framework's**," 2023 International Conference on Computing, Communication, and Intelligent Systems (ICCCIS), Greater Noida, India, 2023, pp. 135-140, doi: 10.1109/ICCCIS60361.2023.10425398.
- ✓ P. Srivastava, A. Agrawal and P. Gaur, "**Cost Optimization of EV Charging Station Integrated with Solar PV**," International Conference on Electrical Power and Energy Systems-2024 (ICEPES), Bhopal, India, 2024.(ACCEPTED)
- ✓ Submitted on the topic “**Optimal Control of Coupled Motors using PAM Signals in Presence of Stochastic Noises**” which was published in IEEE Conference,2014 with IEEE Catalog Number: CFP1458X-POD and ISBN: 978-1-4799-3141-5.
- ✓ Paper published on topic “**Color Image Enhancement using Partial Differential Equation**” in “**International Journal for Innovations in Engineering, Science and Management**” with ISSN2347-7911.
- ✓ M.Tech thesis is submitted under the guidance of **Prof. Smriti Srivastava** and **Prof. Harish Parthasarthy**.

WORKSHOP/FDP/STC and other CERTIFICATES

- ✓ Completed one week short term course on "**Integration of Power Electronics and Renewable Energy for E-Mobility**" (IPEREEM - 2024).
- ✓ Completed 5-days short-term-course of “**Emerging Applications of AI and IoT: Opportunities and Challenges-2023**” at NSUT,Delhi.
- ✓ Completed 5-days FDP of “**Application of Artificial Intelligence on VLSI and Communication Technology(AAIVCT-2022)**”.
- ✓ Participated in pre-screening round in **Tycothon-2021**.

- ✓ Translation certificate from the NPTEL for “Op-Amp Practical Applications: Design, Simulation and Implementation” course.
- ✓ Completed two ATAL FDPs on topics “Artificial Intelligence” and “Robotics”.
- ✓ Course on “Embedded Systems & Its Utilization” of four days program.
- ✓ Workshop coordinated & conducted on “VLSI & Its Application” of one day program.
- ✓ Workshop attended on “VLSI & Its Application” of one day program.
- ✓ Participated “7th National Seminar on Ancient Indian Science & Technology” at IIT Kanpur.
- ✓ Workshop attended on “Universal Human Values & Professional Ethics” of eight-day program.
- ✓ Completed 5 days Faculty Development Program- Student Induction on UHV Refresher1 (Part 1) as online.

WORKSHOP ORGANISED

- ✓ Voluntarily participated in TENSYP 2024 conference with an organizing team.
- ✓ **MATLAB Program** and such various programs also organized at college level.
- ✓ 8 days’ workshop on “Universal Human Values & Professional Ethics” also organized at college level.

COLLEGE RELATED DUTIES

- ✓ Working as **Class Coordinator & Lab Incharge**.
- ✓ Worked in **Discipline Committee** and **Placement Cell**.
- ✓ Enthusiastically participated in **Cultural Programs** at College level.

AREA OF INTEREST (Subjects of EC, CS and EE department)

- | | |
|------------------------------------|------------------------------------|
| ✓ Digital Electronics | ✓ Microprocessor & Microcontroller |
| ✓ Electronics devices & Circuits | ✓ Optical Network |
| ✓ Data Communication Network | ✓ Electromagnetic Field Theory |
| ✓ Analog and Digital Communication | ✓ Electronic Switching |

SNAPSHOTS

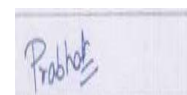
- ✓ ... **Electronics & Communication** with significant knowledge of ...Programming languages: **MATLAB, LABVIEW, Android, Typhoon, Cadence Pspice, C-language** etc
- ✓**GATE-2019** qualified in **Electronics & Communication** branch with **GATE score 419**.
- ✓**GATE-2013** qualified in **Electronics & Communication** branch with **GATE score 405**.
- ✓ ...A self motivated team player with excellent communication, analytical, customer relation management skills and problem solving and trouble shooting skills.

PERSONAL DETAILS

Date of birth	: 07 th August ,1987
Father’s Name	: Mr. Vinod KumarSrivastava
Correspondence address	: E-581,SECTOR-I,L.D.A Colony, Kanpur Road, Lucknow-226012

DECLARATION

I declared that all the above mentioned information is true and best of knowledge and belief.



(PrabhatSrivastava)